Power MOSFET 80 Amps, 60 Volts N-Channel D²PAK, TO-220

Features

- Low R_{DS(on)}
- High Current Capability
- Avalanche Energy Specified
- These are Pb-Free Devices

Applications

- LED Lighting and LED Backlight Drivers
- DC-DC Converters
- DC Motor Drivers
- Power Supplies Secondary Side Synchronous Rectification

MAXIMUM RATINGS (T_J = 25°C Unless otherwise specified)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	60	V
Gate-to-Source Voltage	ge – Conti	nuous	V _{GS}	±20	V
Gate-to-Source Volta (T _P < 10 μs)	ge – Nonre	epetitive	V _{GS}	±30	V
Continuous Drain	Steady State	T _C = 25°C	I _D	80	Α
Current R _{0JC} (Note 1)	State	T _C = 100°C		61	
Power Dissipation R _{0JC} (Note 1)	Steady State	T _C = 25°C	P _D	166	W
Pulsed Drain Current	llsed Drain Current $t_p = 10 \mu s$			185	Α
Operating and Storage Temperature Range			T _J , T _{stg}	–55 to 175	°C
Source Current (Body Diode)			Is	75	Α
Single Pulse Drain-to-Source Avalanche Energy – Starting $T_J = 25^{\circ}C$ ($V_{DD} = 50 \ V_{dc}, \ V_{GS} = 60 \ V_{dc}, \ I_{L(pk)} = 75 \ A, L = 0.1 \ mH, \ R_G = 25 \ \Omega)$			E _{AS}	280	mJ
Lead Temperature for Soldering Purposes, 1/8" from Case for 10 Seconds			T _L	260	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case (Drain) Steady State (Note 1)	$R_{\theta JC}$	0.9	°C/W
(Note 1)	$R_{\theta JA}$	43	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

 Surface mounted on FR4 board using 1 sq in pad size, (Cu Area 1.127 sq in [1 oz] including traces).

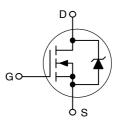


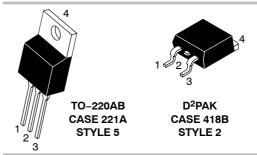
ON Semiconductor®

http://onsemi.com

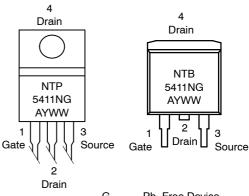
V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX (Note 1)
60 V	10 m Ω @ 10 V	80 A

N-Channel





MARKING DIAGRAM & PIN ASSIGNMENT



G = Pb-Free Device A = Assembly Location Y = Year

Y = Year WW = Work Week

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

ELECTRICAL CHARACTERISTICS ($T_J = 25$ °C Unless otherwise specified)

Characteristics	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				•		•
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{DS} = 0 V,	I _D = 250 μA	60			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				54.2		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	T _J = 25°C			10	μΑ
		$V_{DS} = 60 \text{ V}$	T _J = 150°C			100	1
Gate-Body Leakage Current	I _{GSS}	V _{DS} = 0 V, V	′ _{GS} = ±20 V			±100	nA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(th)}	$V_{GS} = V_{DS}$	I _D = 250 μA	2.0	3.2	4.0	V
Negative Threshold Temperature Coefficient	V _{GS(th)} /T _J				6.6		mV/°C
Drain-to-Source On Voltage	V _{DS(on)}	V _{GS} = 10 \	/, I _D = 80 A		0.71	0.92	V
		V _{GS} = 10 V, I _D	= 40 A, 150°C		0.65		1
Static Drain-to-Source On-Resistance	R _{DS(on)}	V _{GS} = 10 \	/, I _D = 40 A		8.4	10	mΩ
Forward Transconductance	9FS	V _{GS} = 15 \	/, I _D = 40 A		70		S
CHARGES, CAPACITANCES & GATE RESIST	ANCE			1	•	•	•
Input Capacitance	C _{iss}	V _{DS} = 25 V	, V _{GS} = 0 V,		3365	4500	pF
Output Capacitance	C _{oss}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1 MHz			615		1
Transfer Capacitance	C _{rss}				230		
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V,	V _{DS} = 48 V,		92	130	nC
Threshold Gate Charge	Q _{G(TH)}	I _D =	80 Å		4.1		1
Gate-to-Source Charge	Q _{GS}				19		- -
Gate-to-Drain Charge	Q_{GD}				43		
SWITCHING CHARACTERISTICS, V _{GS} = 10 V	(Note 3)			I		1	
Turn-On Delay Time	t _{d(on)}	V _{GS} = 10 V, V _{DD} = 48 V,			22		ns
Rise Time	t _r	I _D = 80 A,	$R_G = 9.1 \Omega$		122		1
Turn-Off Delay Time	t _{d(off)}				116		1
Fall Time	t _f				113		
DRAIN-SOURCE DIODE CHARACTERISTICS)						
Forward Diode Voltage	V_{SD}	V _{GS} = 0 V	T _J = 25°C		0.91	1.1	V_{dc}
		I _S = 37.5 A	T _J = 150°C		0.8		1
Reverse Recovery Time	t _{rr}	$I_S = 37.5 A_{dc}, V_{GS} = 0 V_{dc},$ $dI_S/dt = 100 A/\mu s$			62		ns
Charge Time	ta				43		1
Discharge Time	t _b				19		1
Reverse Recovery Stored Charge	Q _{RR}				0.15		μС

^{2.} Pulse Test: Pulse Width = 300 μ s, Duty Cycle = 2%. 3. Switching characteristics are independent of operating junction temperatures.

TYPICAL PERFORMANCE CURVES

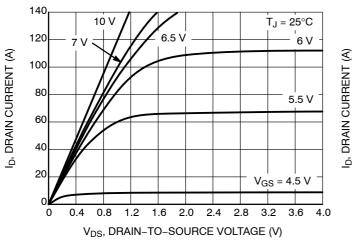


Figure 1. On-Region Characteristics

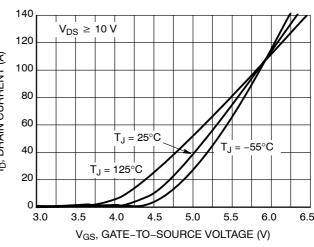


Figure 2. Transfer Characteristics

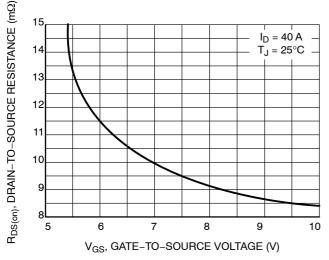


Figure 3. On-Resistance vs. Gate-to-Source Voltage

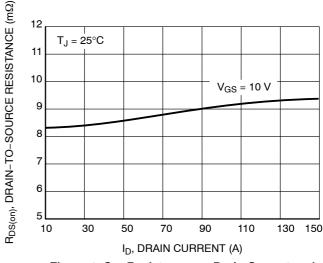


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

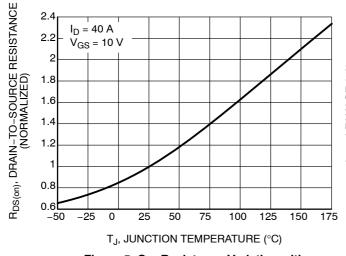


Figure 5. On–Resistance Variation with Temperature

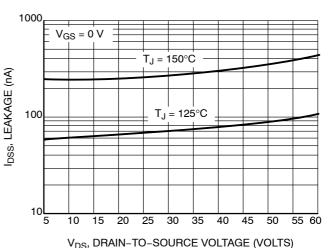


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL PERFORMANCE CURVES

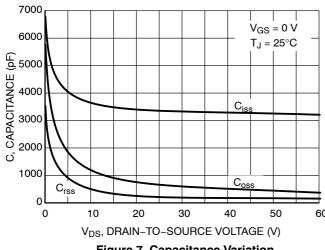


Figure 7. Capacitance Variation

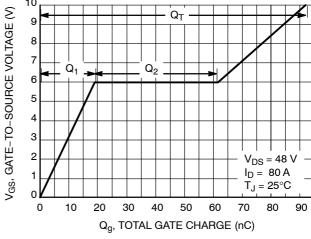


Figure 8. Gate-to-Source Voltage vs. Total Charge

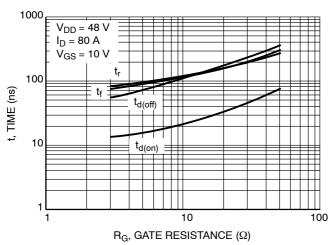


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

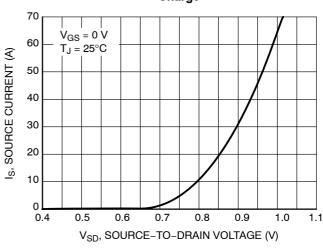


Figure 10. Diode Forward Voltage vs. Current

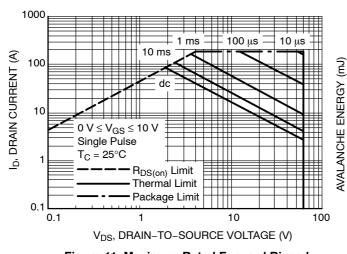


Figure 11. Maximum Rated Forward Biased Safe Operating Area

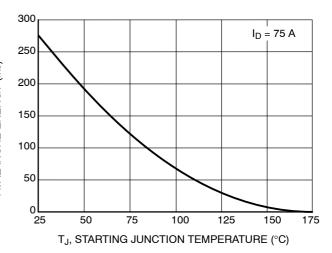


Figure 12. Maximum Avalanche Energy vs. **Starting Junction Temperature**

TYPICAL PERFORMANCE CURVES

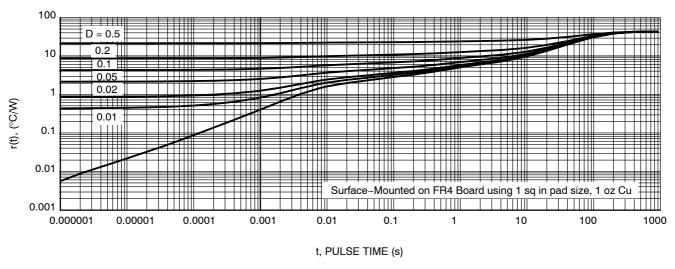


Figure 13. Thermal Response

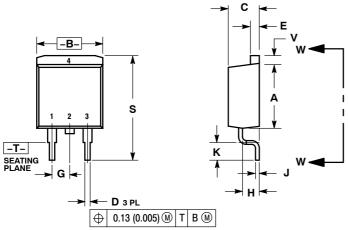
ORDERING INFORMATION

Device	Package	Shipping [†]
NTP5411NG	TO-220AB (Pb-Free)	50 Units / Rail
NTB5411NT4G	D ² PAK (Pb-Free)	800 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

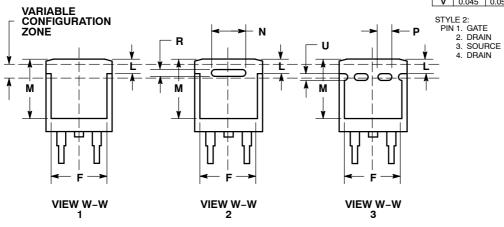
PACKAGE DIMENSIONS

D²PAK 3 CASE 418B-04 **ISSUE K**

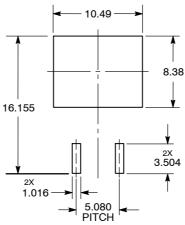


- NOTES:
 1. DIMENSIONING AND TOLERANCING
- PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. 418B-01 THRU 418B-03 OBSOLETE, NEW STANDARD 418B-04.

	INCHES		MILLIMETERS			
DIM	MIN	MAX	MIN	MAX		
Α	0.340	0.380	8.64	9.65		
В	0.380	0.405	9.65	10.29		
С	0.160	0.190	4.06	4.83		
D	0.020	0.035	0.51	0.89		
Е	0.045	0.055	1.14	1.40		
F	0.310	0.350	7.87	8.89		
G	0.100	BSC	2.54 BSC			
Н	0.080	0.110	2.03	2.79		
J	0.018	0.025	0.46	0.64		
Κ	0.090	0.110	2.29	2.79		
L	0.052	0.072	1.32	1.83		
М	0.280	0.320	7.11	8.13		
N	0.197	0.197 REF		5.00 REF		
Р	0.079 REF		2.00 REF			
R	0.039 REF		0.99 REF			
S	0.575	0.625	14.60	15.88		
٧	0.045	0.055	1.14	1.40		



SOLDERING FOOTPRINT*

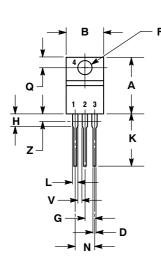


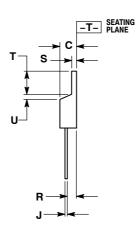
DIMENSIONS: MILLIMETERS

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AF**





- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.570	0.620	14.48	15.75	
В	0.380	0.405	9.66	10.28	
U	0.160	0.190	4.07	4.82	
D	0.025	0.035	0.64	0.88	
F	0.142	0.161	3.61	4.09	
G	0.095	0.105	2.42	2.66	
H	0.110	0.155	2.80	3.93	
7	0.014	0.025	0.36	0.64	
K	0.500	0.562	12.70	14.27	
L	0.045	0.060	1.15	1.52	
N	0.190	0.210	4.83	5.33	
ø	0.100	0.120	2.54	3.04	
R	0.080	0.110	2.04	2.79	
S	0.045	0.055	1.15	1.39	
T	0.235	0.255	5.97	6.47	
5	0.000	0.050	0.00	1.27	
٧	0.045		1.15		
Z		0.080		2.04	

STYLE 5:

PIN 1 GATE

- DRAIN 2.
- 3 SOURCE
- DRAIN

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice on semiconductor and are registered readerlands of semiconductor Components industries, Ite (SCILLC) solicit esserves the right to make changes without further holice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada

Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative